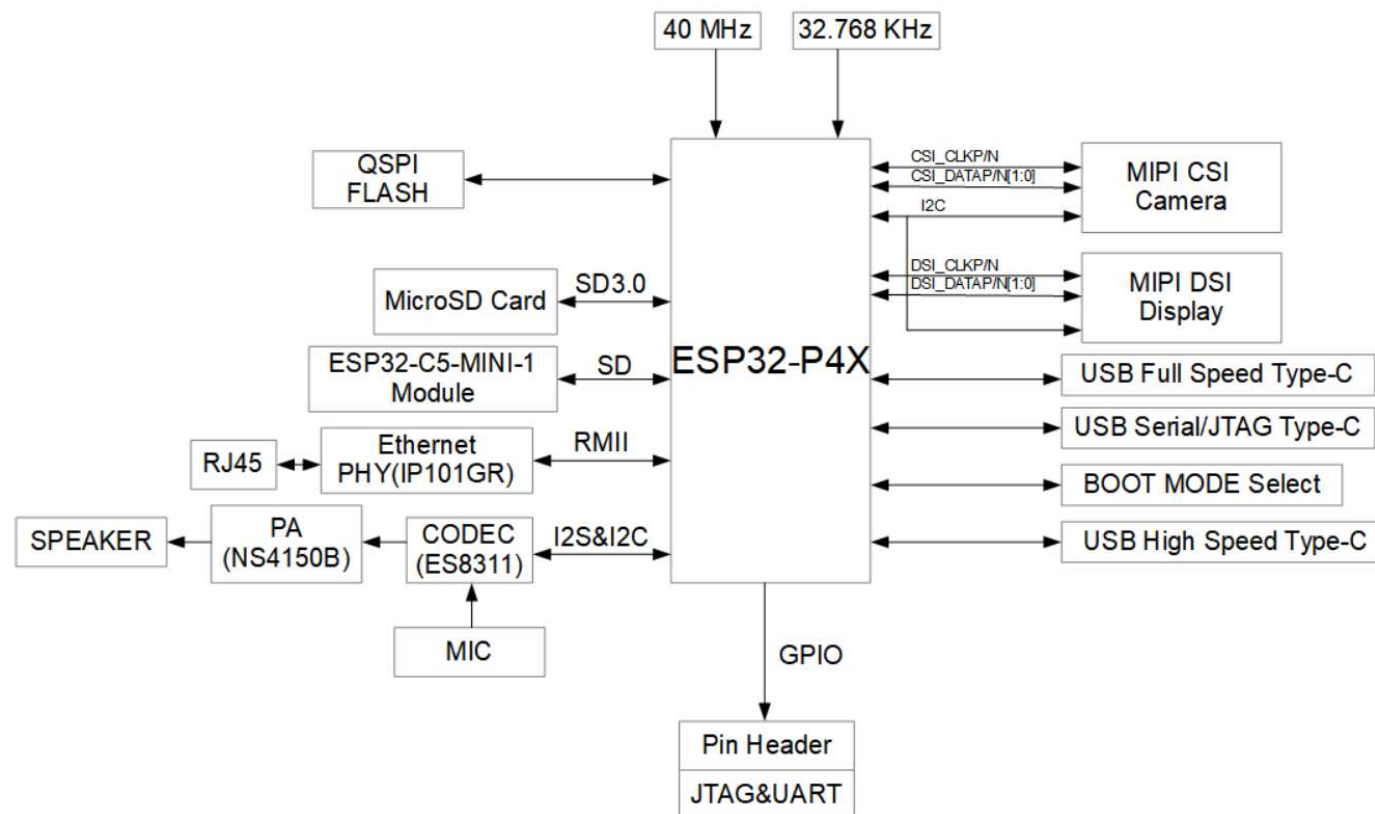
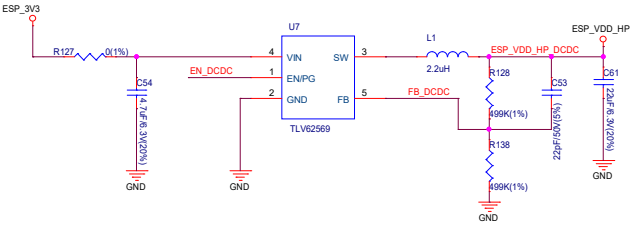
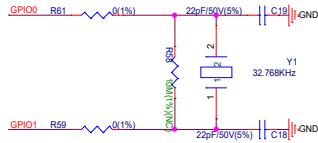
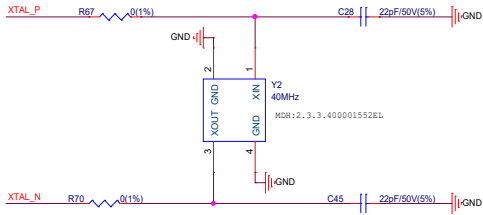


Design Block:

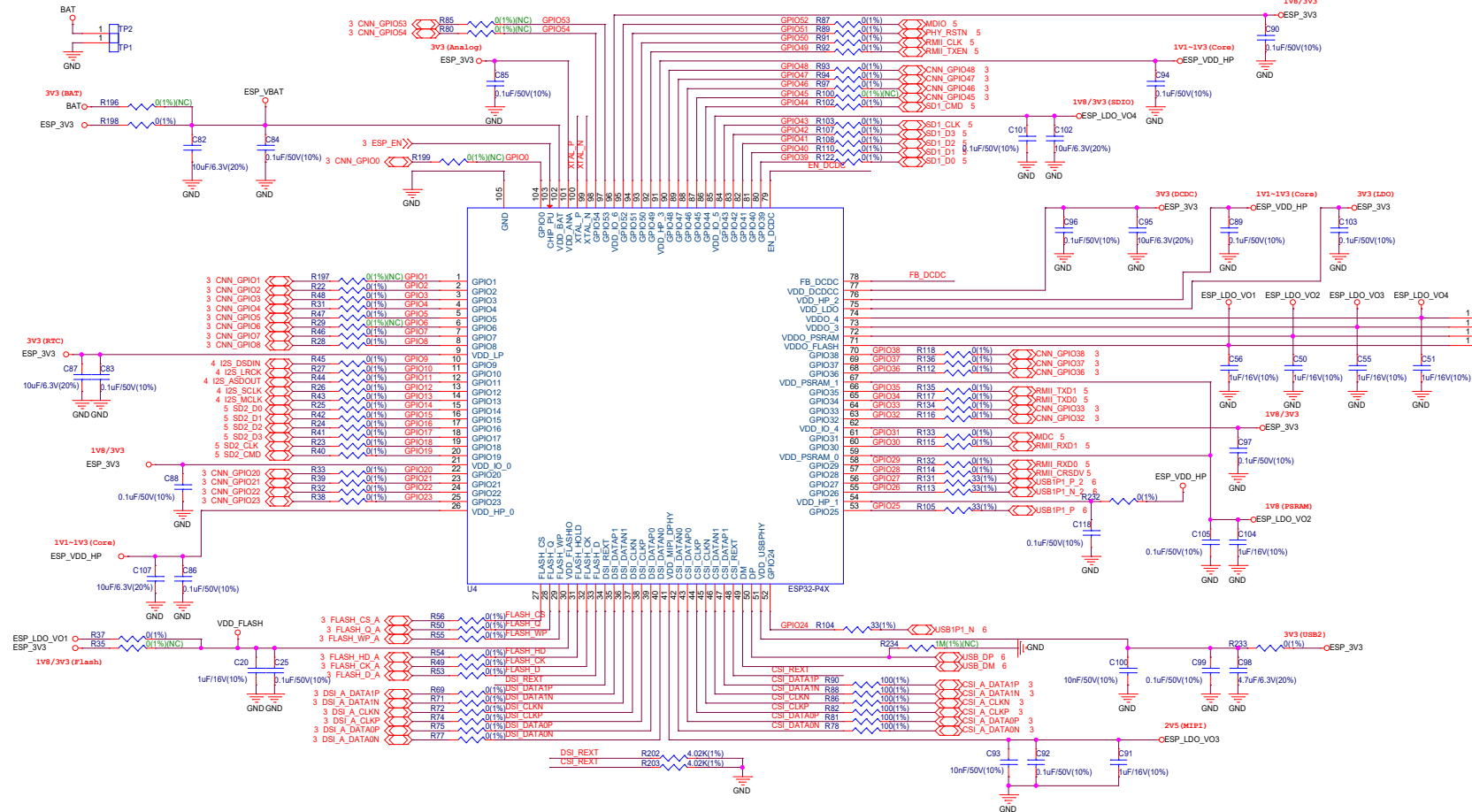




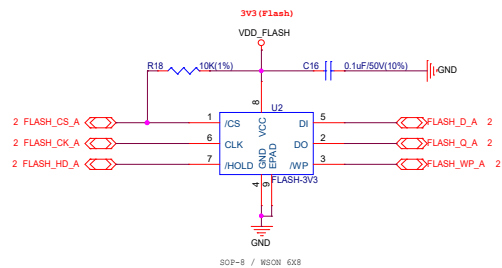
GPIO38 R206 0(1%) GPIO38_BOOTMODE 3
GPIO37 R212 0(1%) GPIO37_BOOTMODE 3
GPIO36 R204 0(1%) GPIO36_BOOTMODE 3
GPIO35 R207 0(1%) GPIO35_BOOTMODE 3
GPIO34 R205 0(1%) GPIO34_BOOTMODE 3

GPIO7 R194 0(1%) ESP_I2C_SDA 3.4
GPIO8 R190 0(1%) ESP_I2C_SCL 3.4
GPIO33 R779 0(1%) PA_CTRL 4
GPIO54 R78 0(1%) CS_EN 5
GPIO6 R30 0(1%) WAKEUP_CS 5

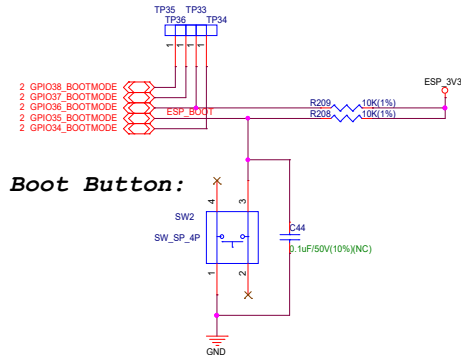
GPIO36 R228 33(1%) CNN_GPIO36 3
GPIO27 R227 33(1%) CNN_GPIO27 3
GPIO45 R231 0(1%) SD_PWRn 5



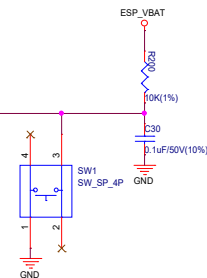
FLASH:



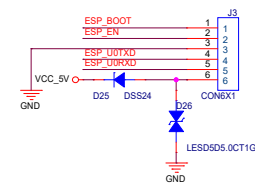
Boot Strapping Pins:



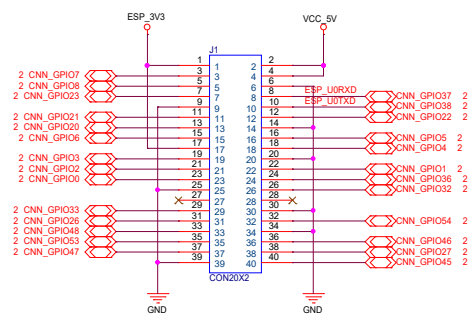
Reset Button:



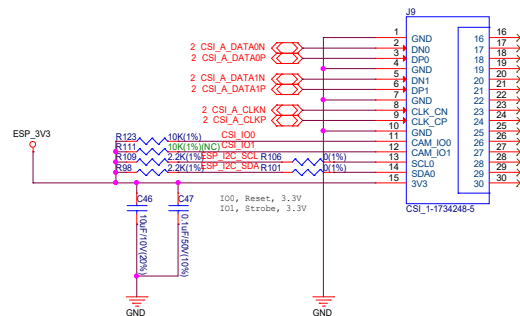
ESP32-P4 Prog Connector:



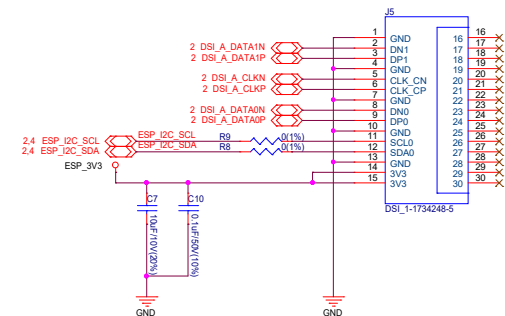
Pin Header For GPIOs:



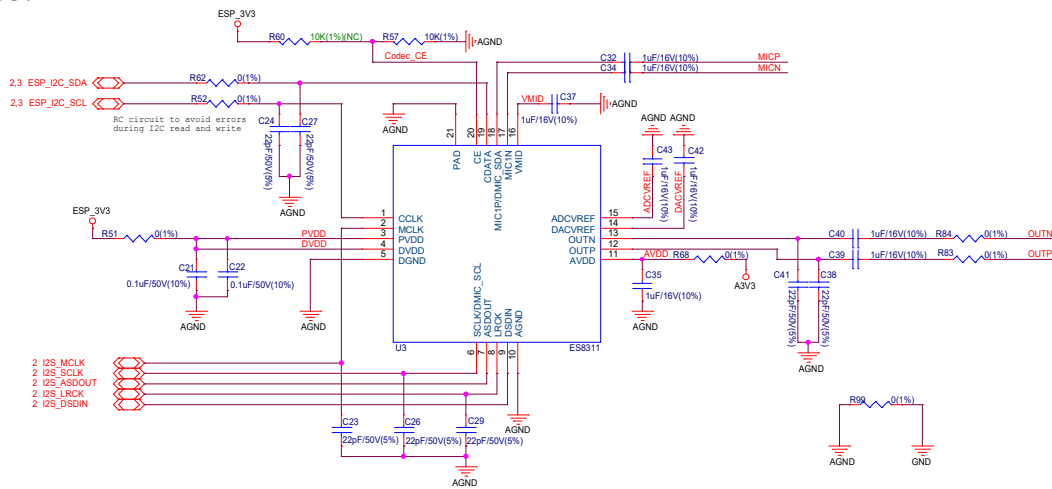
CSI Connector:



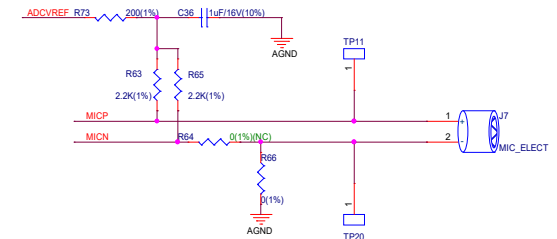
DSI Connector:



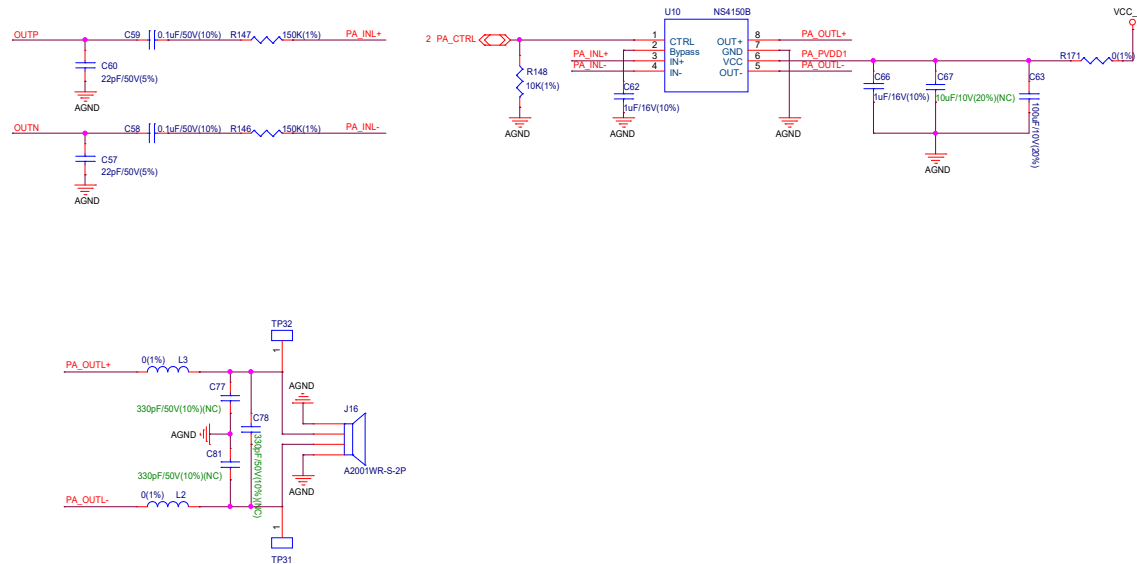
Codec :



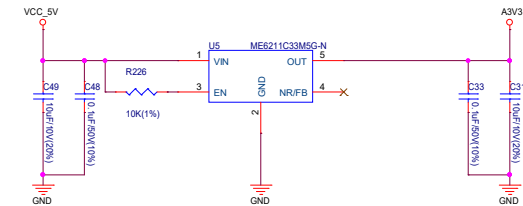
MIC :



PA&SPEAKER :



3.3V LDO For AVDD:



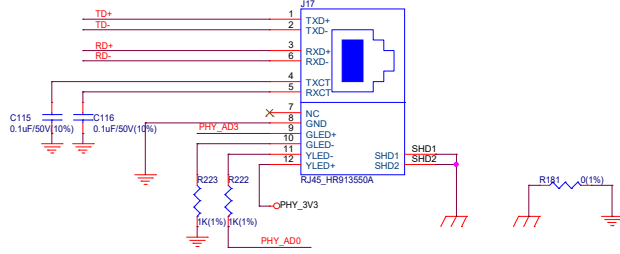
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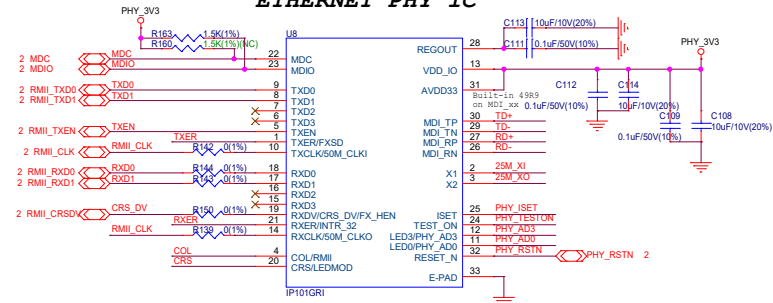
File ESP32-P4X-C5_FUNCTION_EV_BOARD

Size	Page Name	Rev	Proprietary
C	04_Audio	2.0	
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RJ45 with Trans

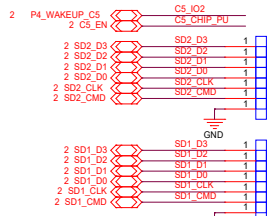
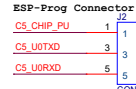
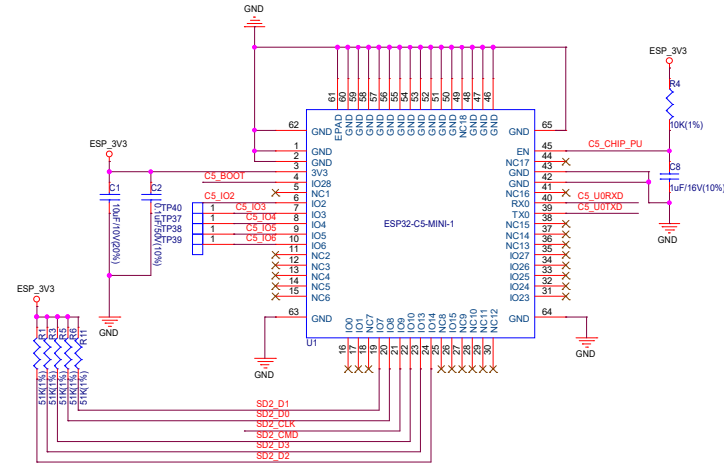


ETHERNET PHY IC

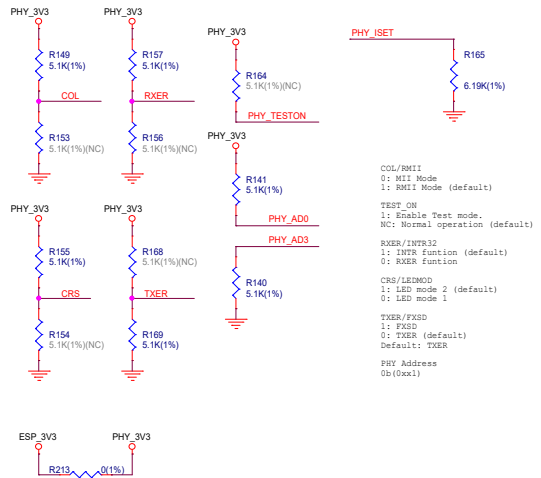


PHY_RSTN is active low.
Set low to PHY_RSTN pin for
at least 10ms will reset all
functions available in
IP101GRI.

WiFi & BT Module:

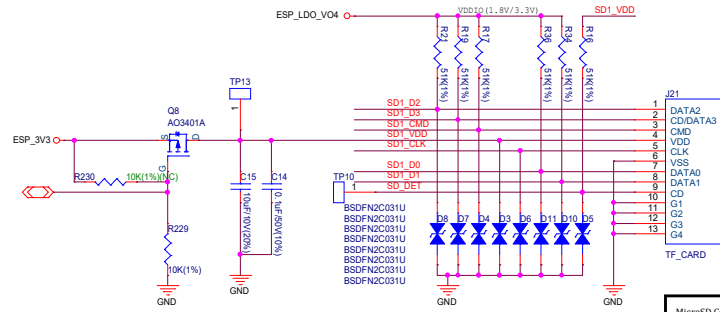


PHY MODE CONFIG



COL/RMII
0: RMII Mode
1: RMII Mode (default)
TEST_ON
1: Enable Test mode.
NC: Normal operation (default)
0: RXER function
CRS/LEDMOD
1: LED mode 2 (default)
0: LED mode 1
TXER/FXSD
1: FXSD
0: TXER (default)
Default: TXER
PHY Address
0b(0x01)

MicroSD Card:



MicroSD Card SPI mode:
1. DAT3 works as CS(chip select);
2. CMD works as D1(Data in);
3. CLK works as Clock;
4. DAT0 works as D0(Data out).

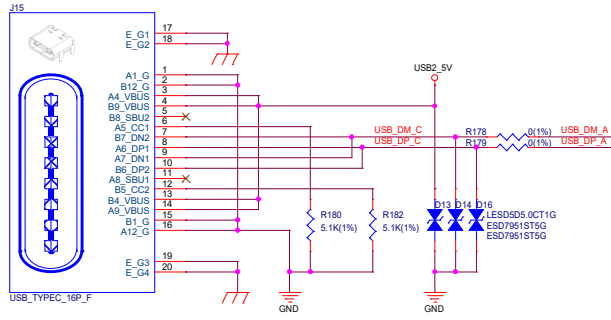
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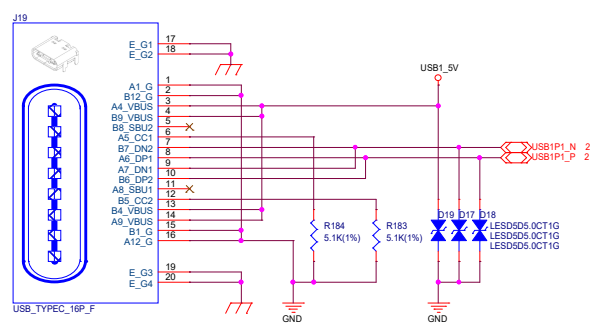
File ESP32-P4X-C5_FUNCTION_EV_BOARD

Size	Page Name	Rev	Proprietary
C	05_Ethernet_SOMMC_WIFI	2.0	
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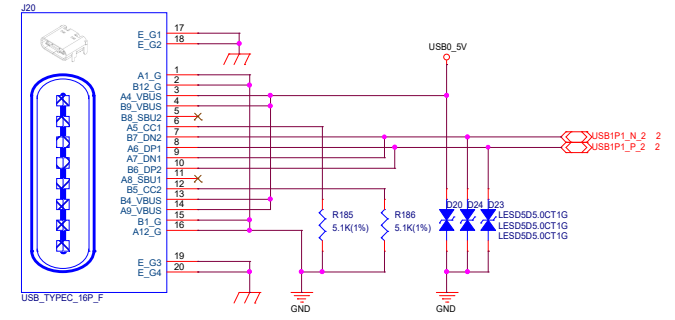
USB High Speed & Power in:



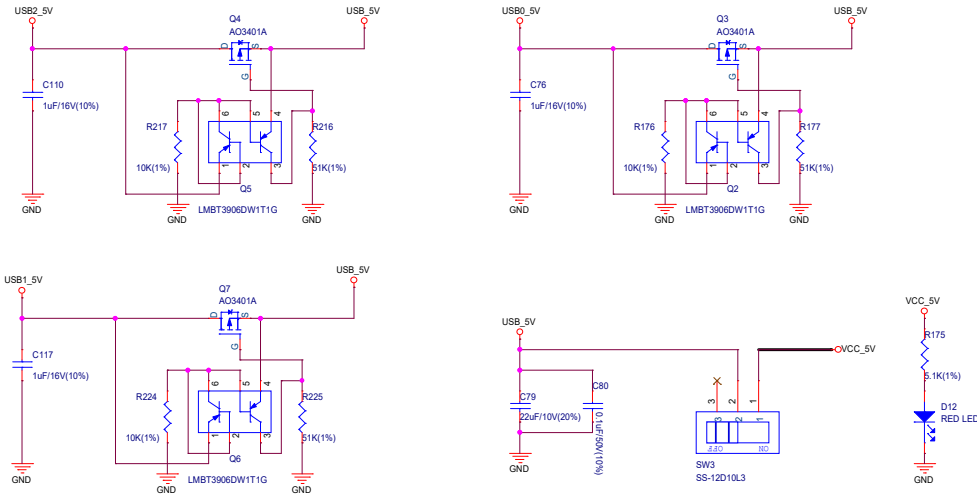
USB Serial/JTAG & Power in:



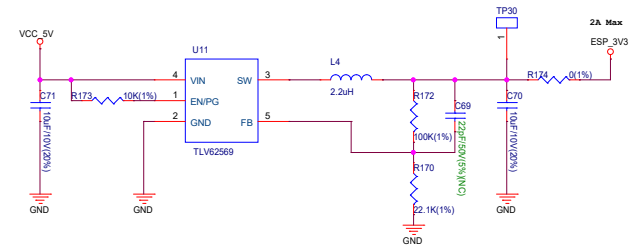
USB Full Speed Slave & Power in:



Ideal Diodes:

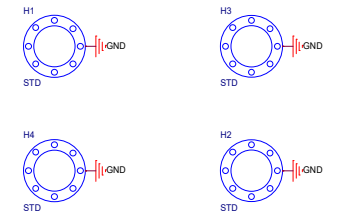
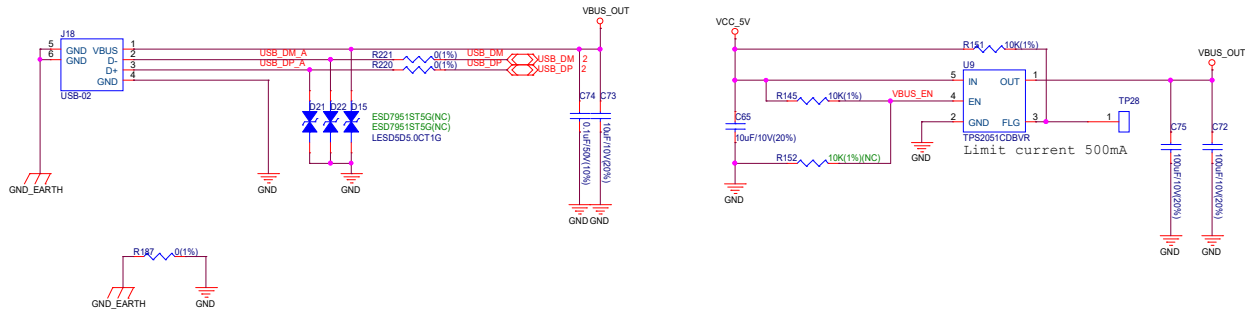


3V3 DC-DC:



USB High Speed Host:

Jumper@J10



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File ESP32-P4X-C5_FUNCTION_EV_BOARD

Size Page Name

C 06_SYS_PWR_USB

Rev 2.0

Proprietary

Date: Thursday, February 28, 2020

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