Notes:
1. EXT_SEL: This input should be tied to VSS in I2C mode.
2. VDD12_CTRL: This pin is the address in I2C mode, VDD12_CTRL should be set to 3.3V/0V to disable/enable the output. This pin functions as the I2C_SCL pin in I2C mode.
3. A 3.3 KΩ resistor from GPIO_2 to DVDD33 can set auto-boot from Standby to Active mode.
4. Apply a 5.8 KΩ resistor from SPI/I2S_SDIB to GND, can set auto-boot from Standby to Active mode.
5. HV_DRV: This input is the power input. The pin functions as the I2C_SCL pin in I2C mode.
6. ZL38063 LDO_EN: This pin is the enable pin for the LDO. The pin functions as the I2C_SCL pin in I2C mode.

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Notes:
1. Vout=0.6*(R1+R2)/R2=5V
2. Ipeak=(180/1.500)*5A
Reset Test

Fixing Holes:

MicroSD:

Connectors For SubBoard:

UART:

SPI & I2C:

JTAG:

I2S:

MicroSD:

Notes:
1. $V_{out}=1.204\times(1+R1/R2)=3.296V$
2. $R1=52.3K, R2=30.1K$ are recommended for better performance.
3. 2.8V0450B from RichTek for second source.