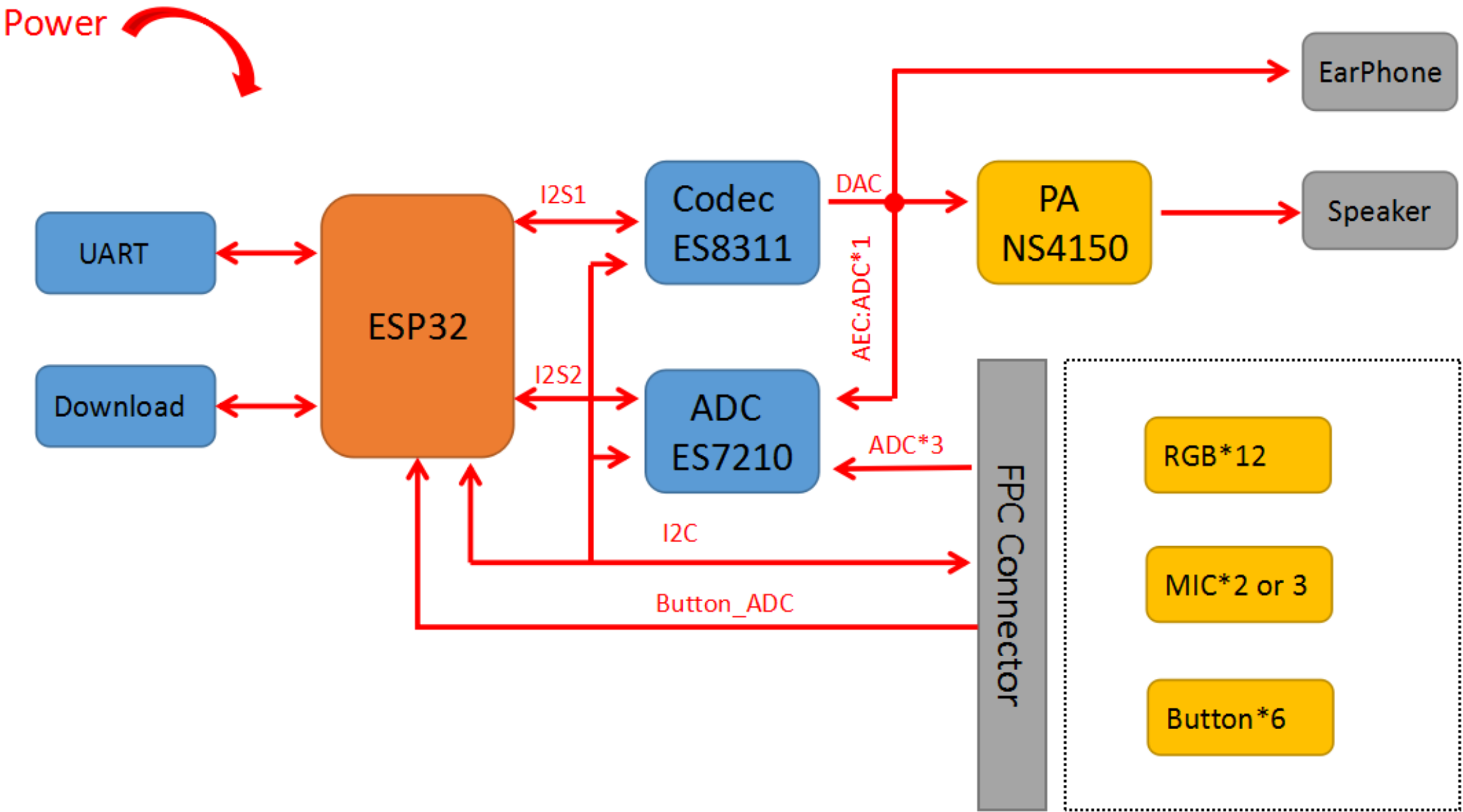
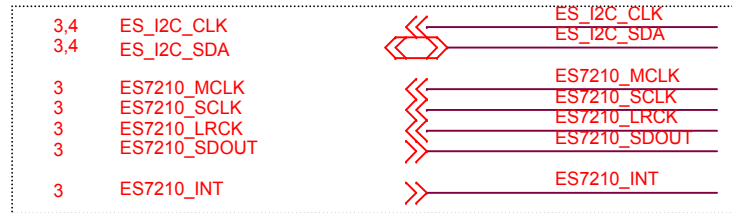


System Block:



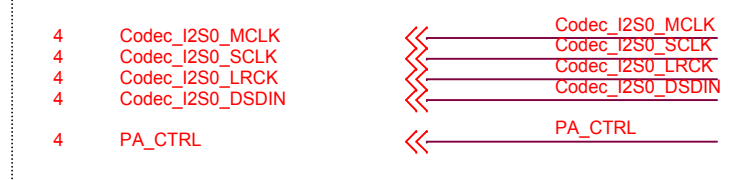
To
ADC



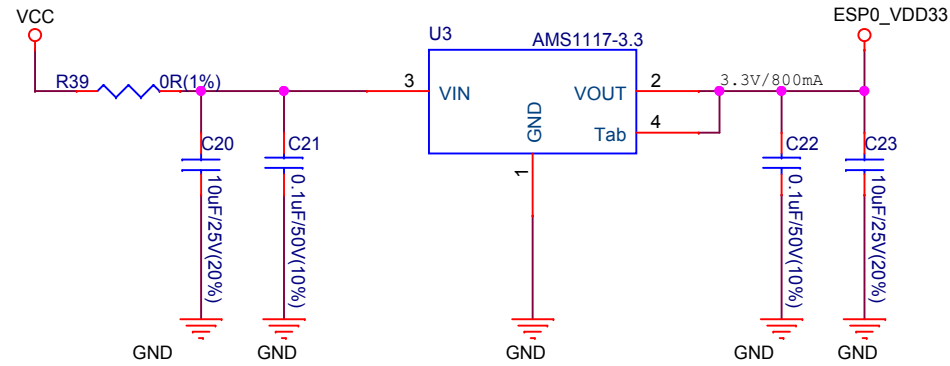
To
Codec



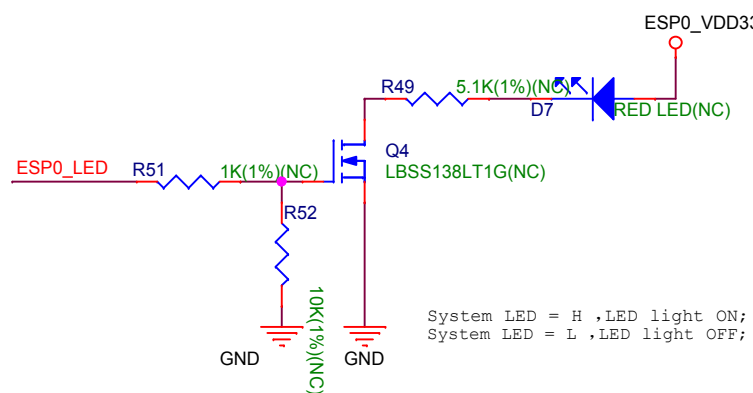
UART
&
Download



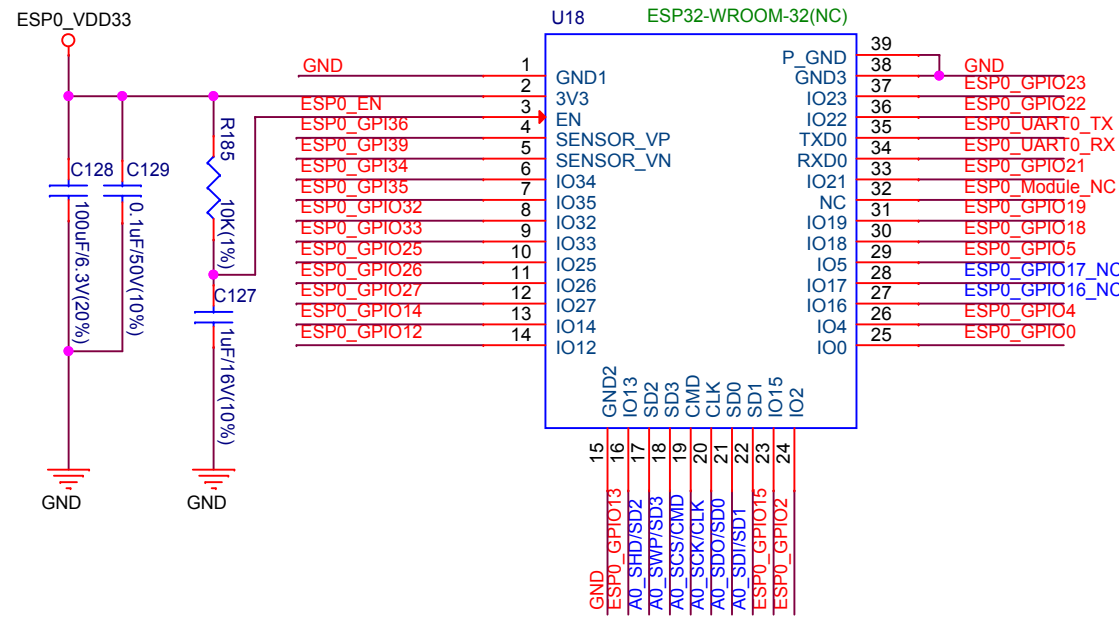
+5V -> 3.3V:



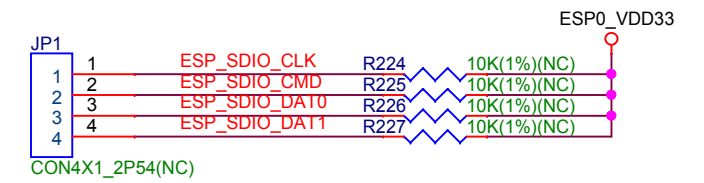
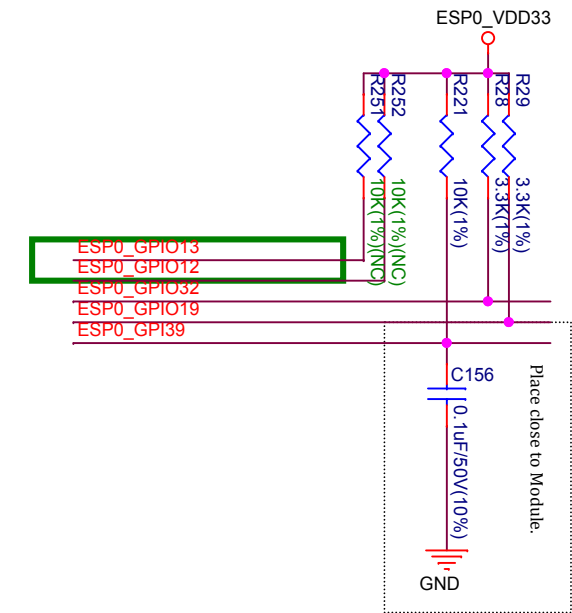
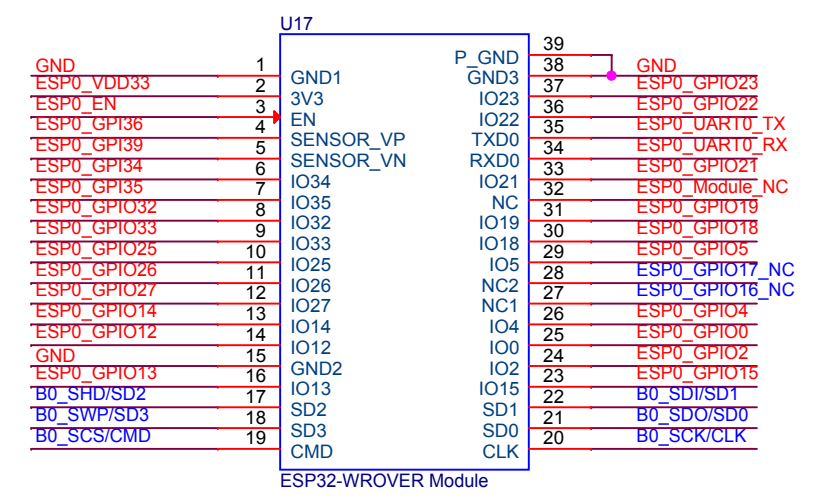
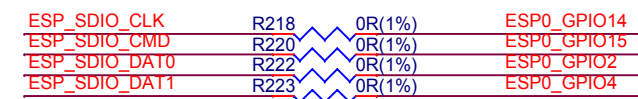
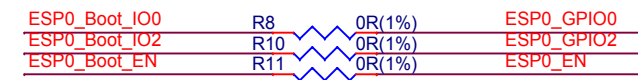
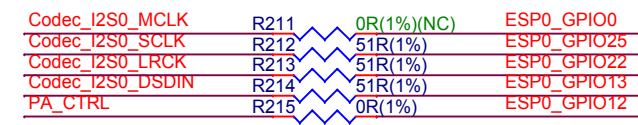
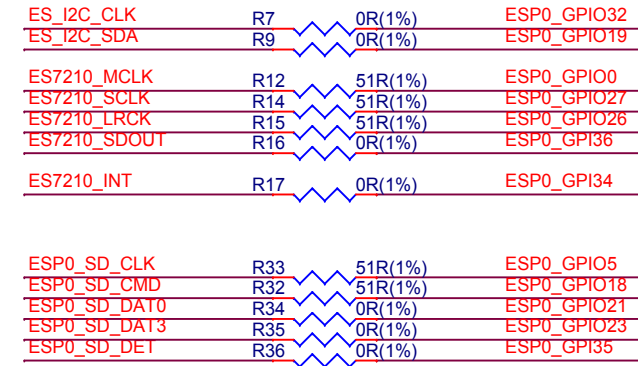
LED Indication:



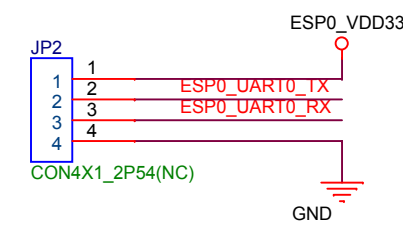
ESP32 Module:



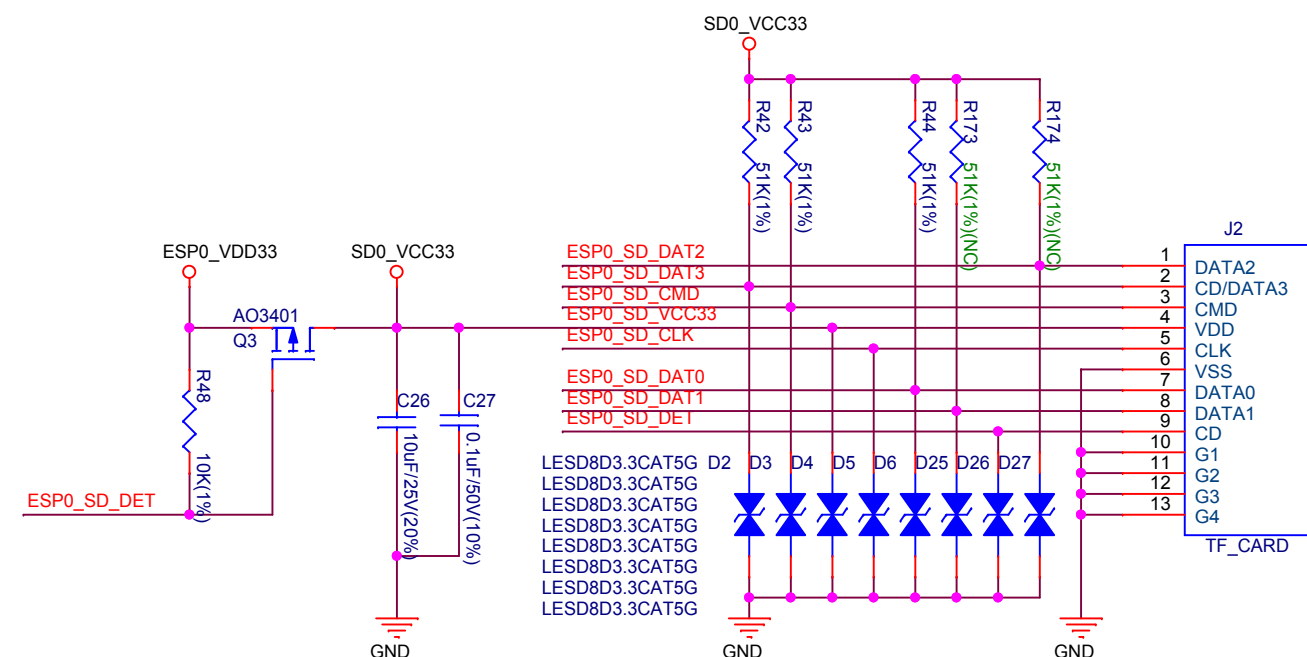
ESP0 Pin Configuration:



Notes:
1. Reserved SDIO 1-bit for communicating with another esp system.



ESP32 Module:



MicroSD Card SPI mode:
1. DAT3 works as CS(chip select);
2. CMD works as DI(Data In);
3. CLK works as Clock;
4. DAT0 works as DO(Data out).

ESPRESSIF

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Title #31025 - ESP32-Korvo

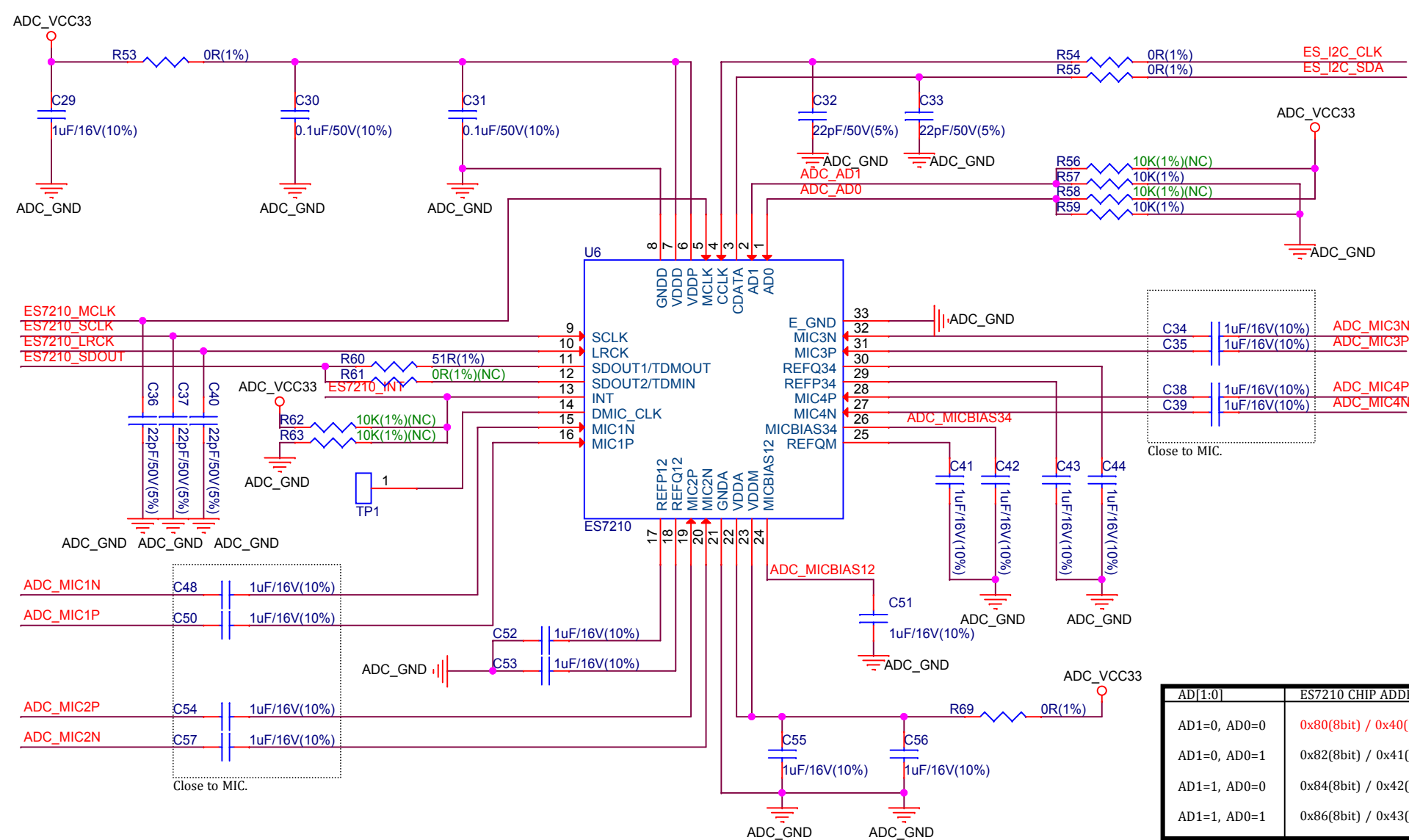
Size Document Number

C <02_ESP0>

Date: Monday, March 16, 2020

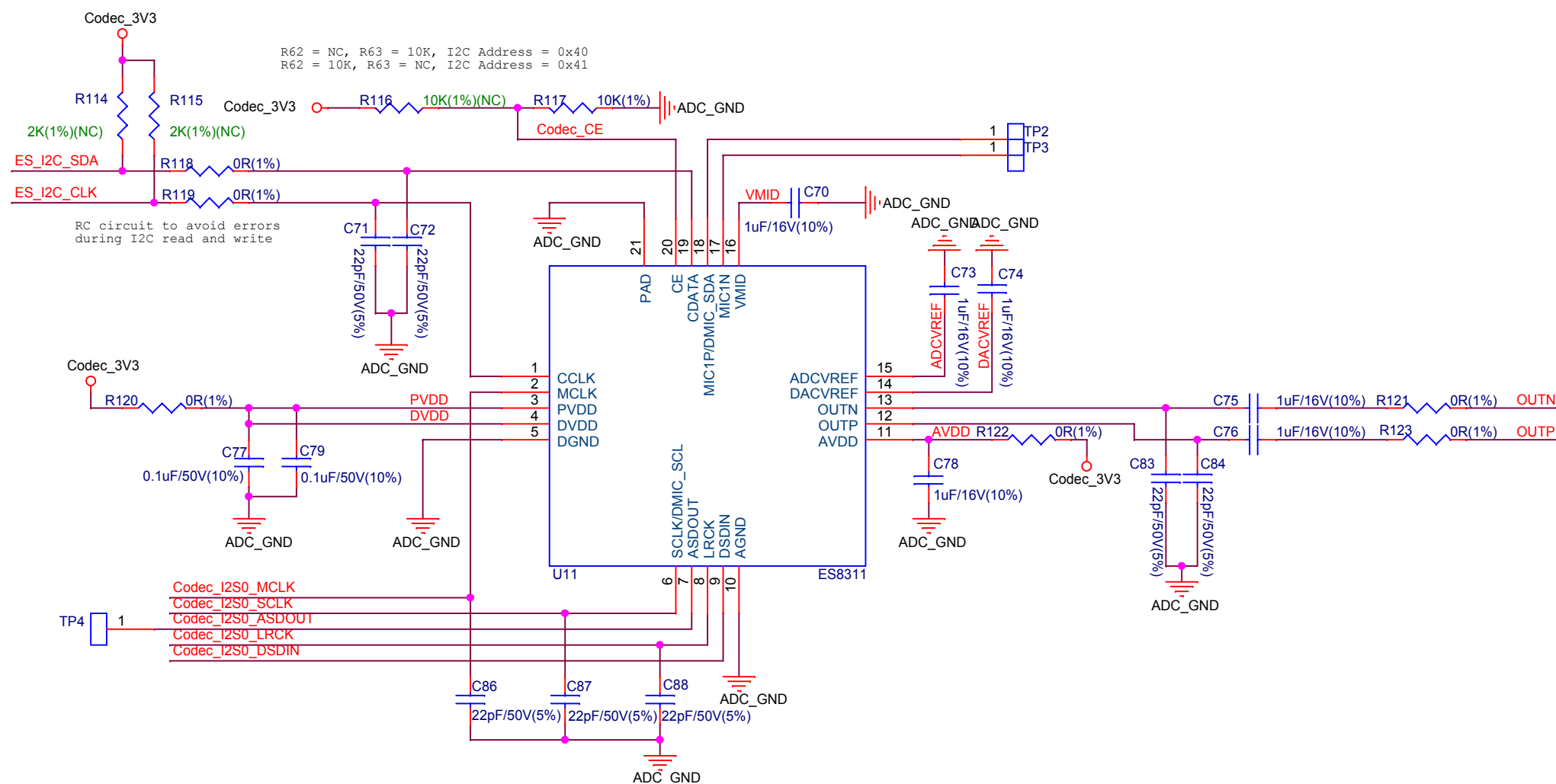
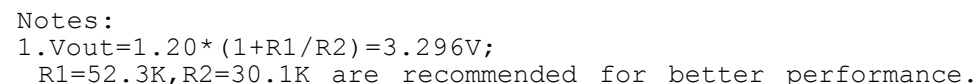
Sheet 2 of 5

Rev V1.1



AD[1:0]	ES7210 CHIP ADDRESS
AD1=0, AD0=0	0x80(8bit) / 0x40(7bit) (Default)
AD1=0, AD0=1	0x82(8bit) / 0x41(7bit)
AD1=1, AD0=0	0x84(8bit) / 0x42(7bit)
AD1=1, AD0=1	0x86(8bit) / 0x43(7bit)





DAC_AOUTLP R131 0R(1%)
 PA_OUTL+ R132 0R(5%)(NC) R133 2.2K(1%) C93 0.47uF/10V(10%) R134 10K(1%) R135 20K(1%) R136 0R(1%) C94 0.22uF/16V(10%) AEC_ADC_MIC3P
 C95 10nF/16V(10%) C96 2200pF/50V(10%) R137 4.3K(1%) R138 100pF/50V(5%) C98 3.3K(1%)(NC)
 PA_OUTL- R140 0R(5%)(NC)
 DAC_AOUTLN R144 0R(1%) R141 2.2K(1%) C102 0.47uF/10V(10%) R142 10K(1%) R145 20K(1%) R143 0R(1%) C103 0.22uF/16V(10%) AEC_ADC_MIC3N

Notes:

- 1.When EarPhone NO plug in, EarPhone Detect keep low, thus Q12 off, and then B=H, AND_PA_CTRL will follow the status of PA_CTRL ;
- 2.When EarPhone plug in, EarPhone Detect Switch to high, thus Q12 ON, and then B=L, AND_PA_CTRL will keep low ;

