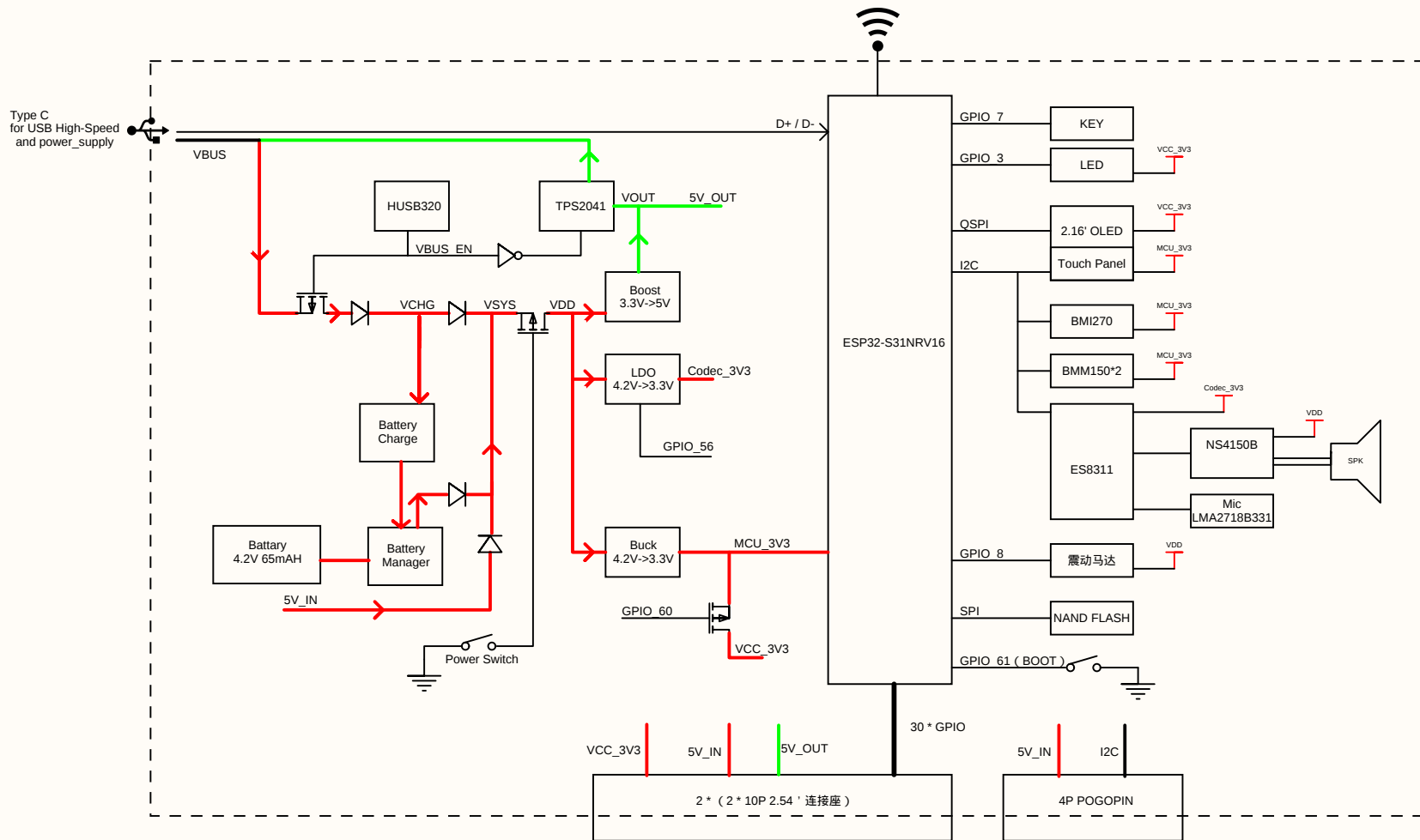
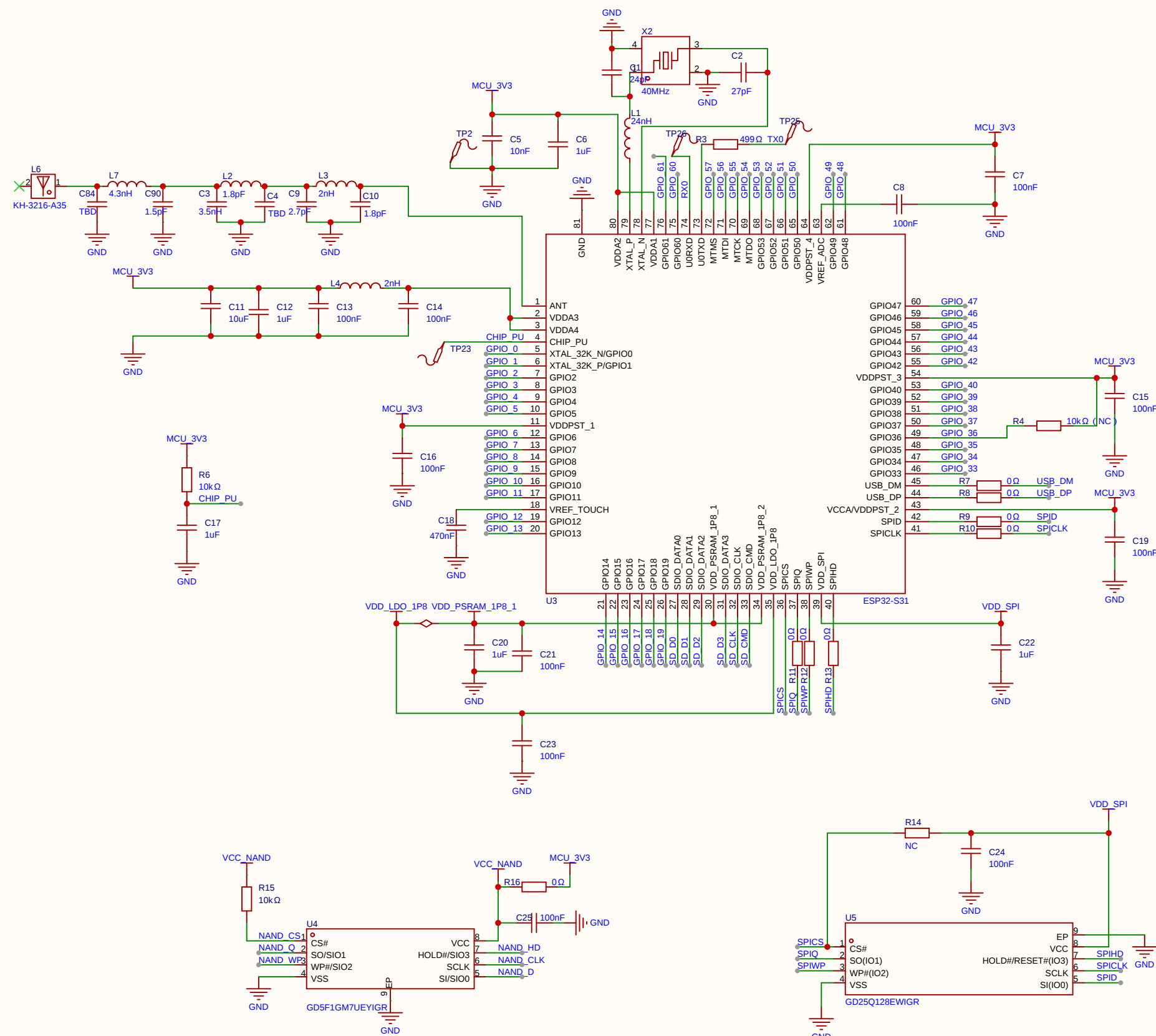
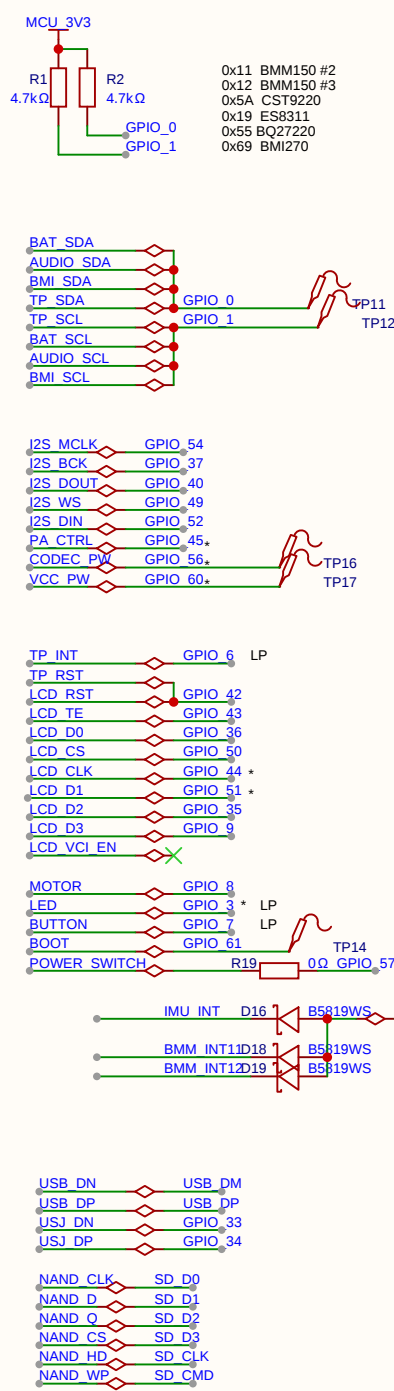




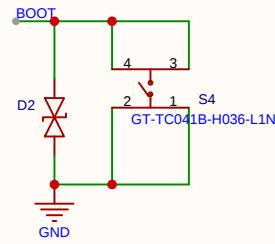
Schematic	SCH_ESP-Mosaico_CoreBoard_V1_0	Create Date	2026-07-23
		Update Date	2026-08-18
Board	ESP-Mosaico_CoreBoard_V1_0	Page Name	Overview
Version		ESP-Mosaico	
Size			
		Page	1
		Count	5
		Espressif	

	复用	引出	内部占用
ADC0 8	GPIO_49	GPIO_46 GPIO_47 GPIO_48	GPIO_42 GPIO_43 GPIO_44 GPIO_45
ADC1 8	GPIO_52 GPIO_54	GPIO_53 GPIO_55	GPIO_50 GPIO_51 GPIO_56 GPIO_57
DAC 2		GPIO_4 GPIO_5	
COMP 4	GPIO_37 GPIO_40	GPIO_38 GPIO_39	
TOUCH 14		GPIO_10 GPIO_11 GPIO_12 GPIO_13 GPIO_14 GPIO_15 GPIO_16 GPIO_17 GPIO_18 GPIO_19	GPIO_6 GPIO_7 GPIO_8 GPIO_9
LPIO 6 GPIO0 GPIO1 GPIO4 GPIO5 GPIO6 GPIO7			GPIO_2 GPIO_3
BOOT			GPIO_61
32K_XTAL	GPIO_0 GPIO_1		
USB-JTAG		USB_DN USB_DP	
STRAP			GPIO_60
GPIO			GPIO_35 GPIO_36
USB			USB_DM USB_DP
UART		RX0 TX0	

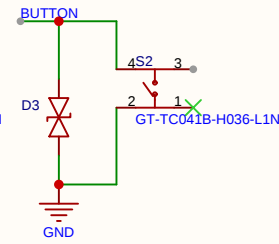


Schematic	SCH_ESP-Mosaico_CoreBoard_V1_0			Create Date	2026-08-04		
				Update Date	2026-08-18		
Board	ESP-Mosaico_CoreBoard_V1_0			Page Name	MCU		
Version		ESP-Mosaico					
Size							
				Page	2	Count	5
 ESPRESSIF				Espressif			

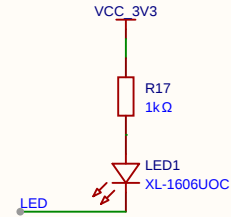
BOOT BUTTON



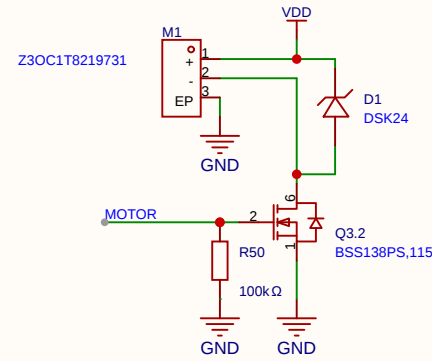
AI-BUTTON



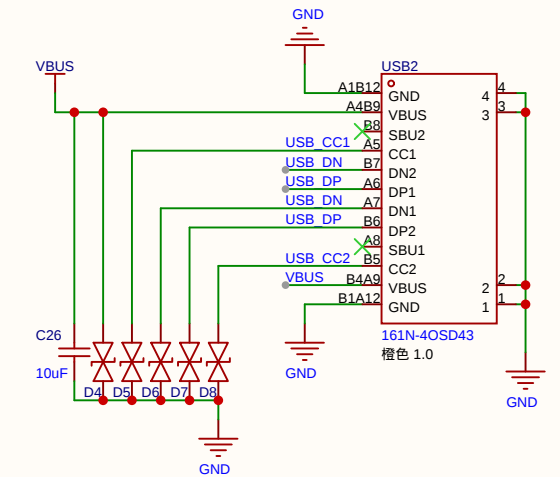
LED



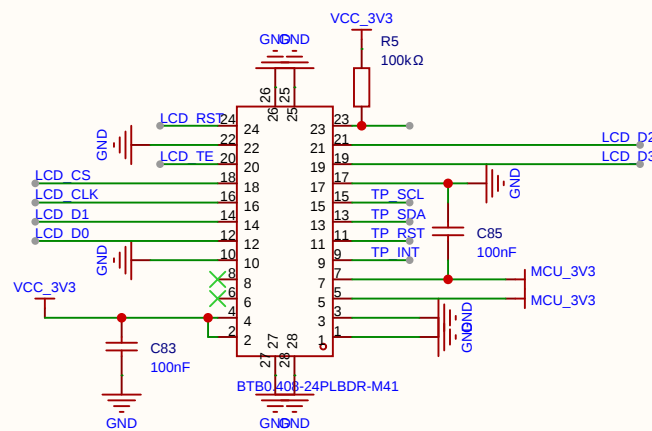
MOTOR



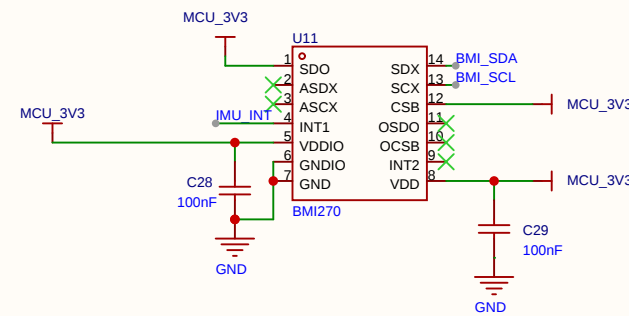
Type-C



LCD

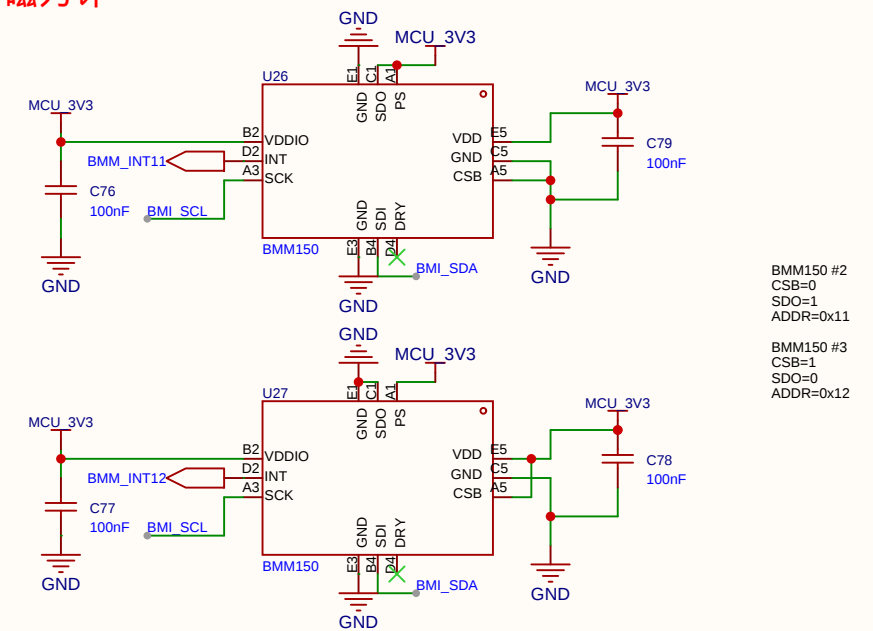


IMU



IIC ADDR : 0x69

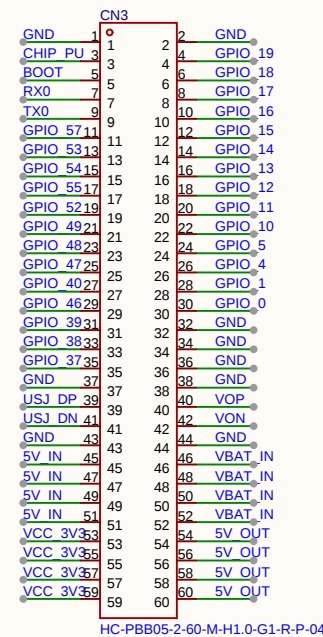
磁力计



```
BMM150 #2
CSB=0
SDO=1
ADDR=0x11

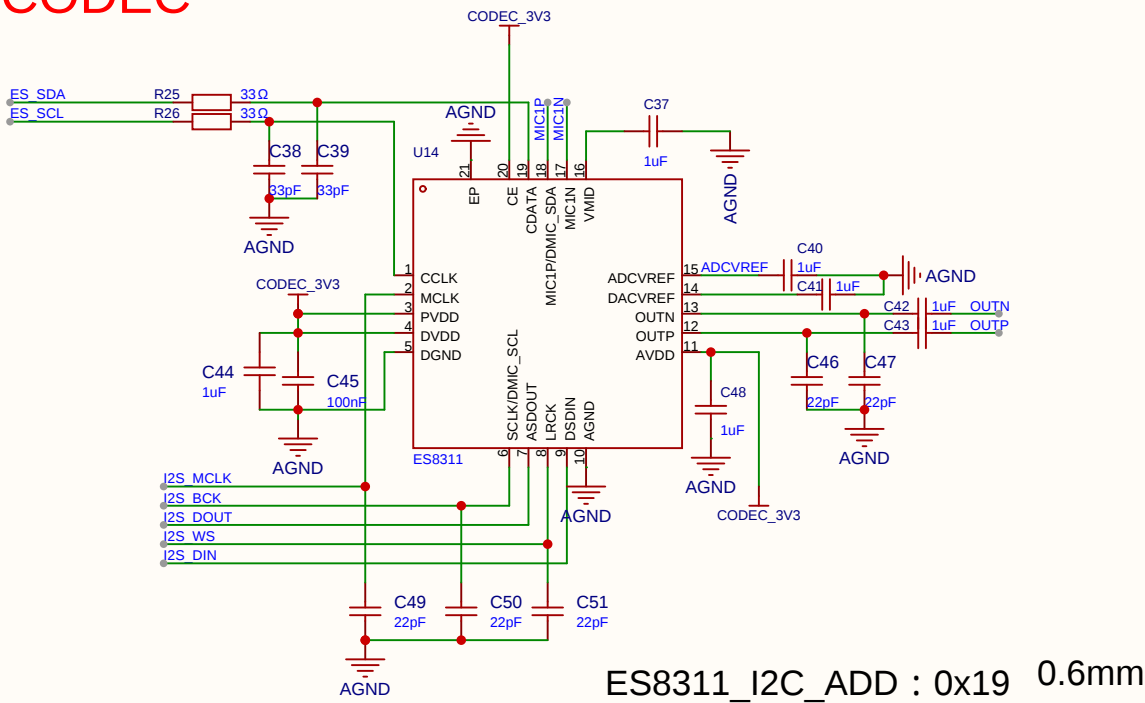
BMM150 #3
CSB=1
SDO=0
ADDR=0x12
```

BTB Connector

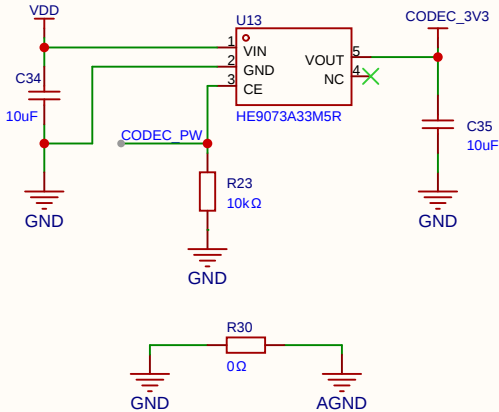


Schematic	SCH_ESP-Mosaico_CoreBoard_V1_0			Create Date	2026-07-30		
				Update Date	2026-08-18		
Board	ESP-Mosaico_CoreBoard_V1_0			Page Name	Interface		
Version		ESP-Mosaico					
Size							
				Page	3	Count	5
 ESPRESSIF				Espressif			

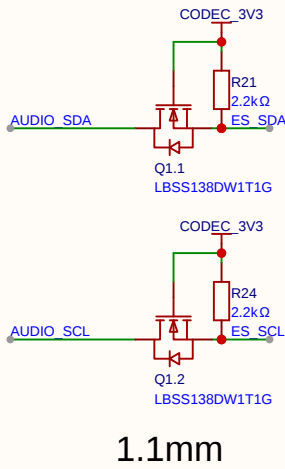
CODEC



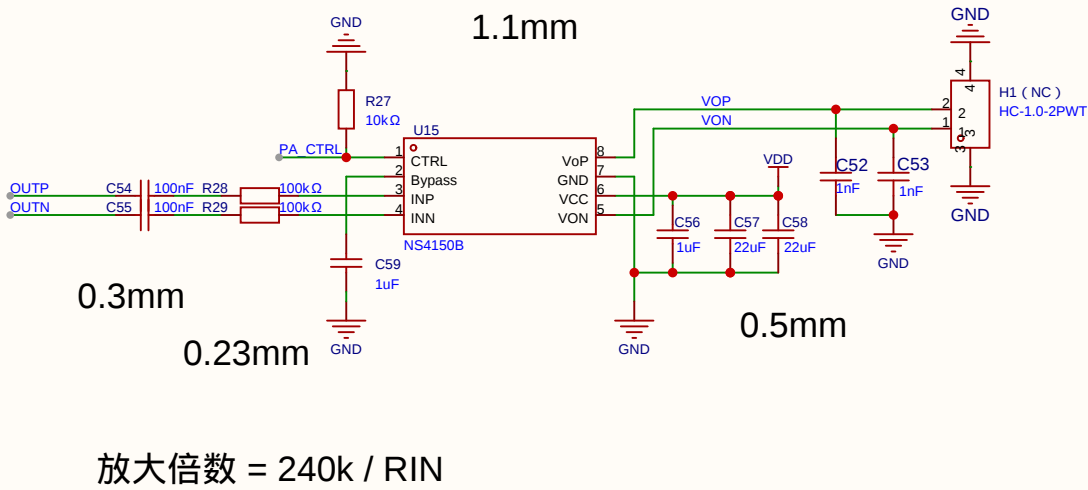
LDO



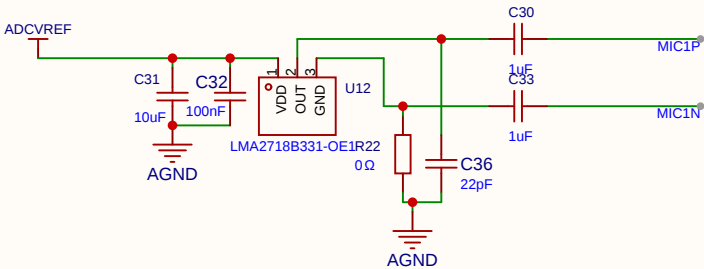
IIC



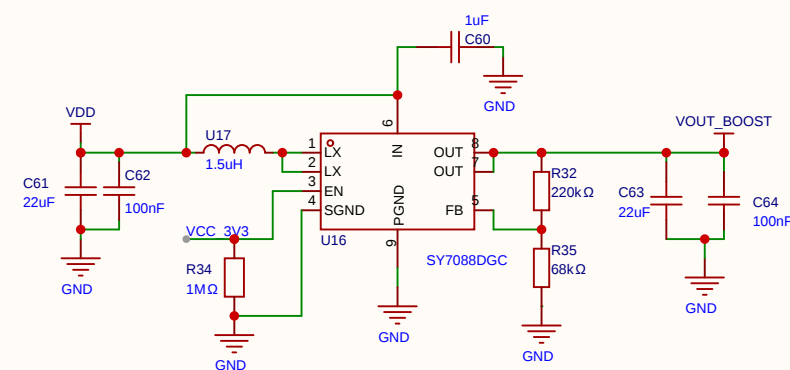
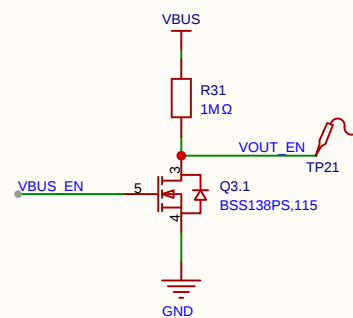
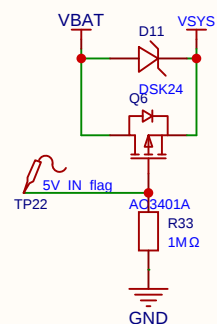
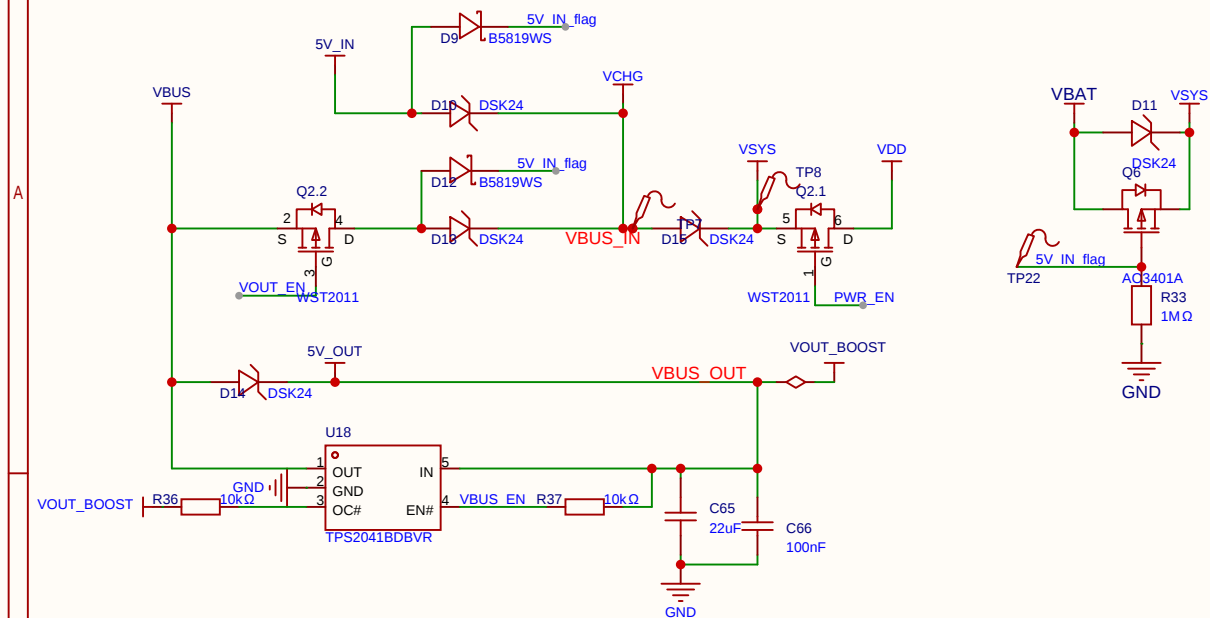
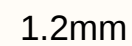
PA



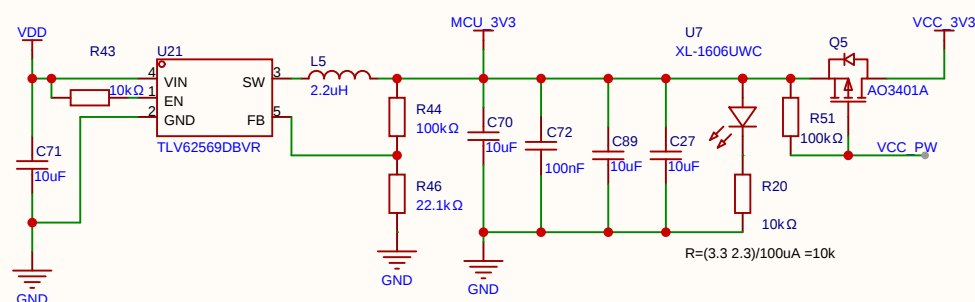
MIC



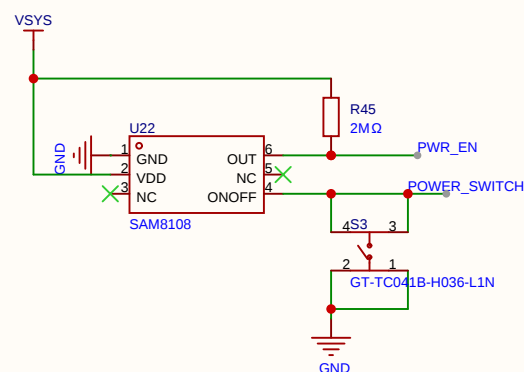
Schematic	SCH_ESP-Mosaico_CoreBoard_V1_0	Create Date	2026-07-23
		Update Date	2026-08-18
Board	ESP-Mosaico_CoreBoard_V1_0	Page Name	Codec
Version		ESP-Mosaico	
Size			
		Page	4 Count 5
		Espressif	



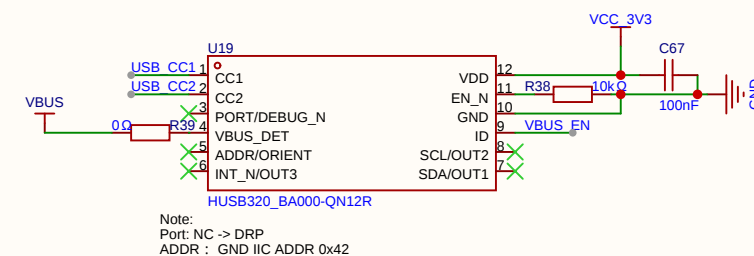
DCDC



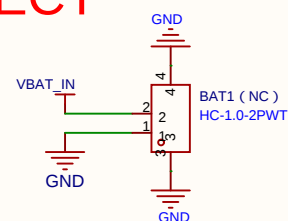
POWER ON/OFF MANAGEMENT



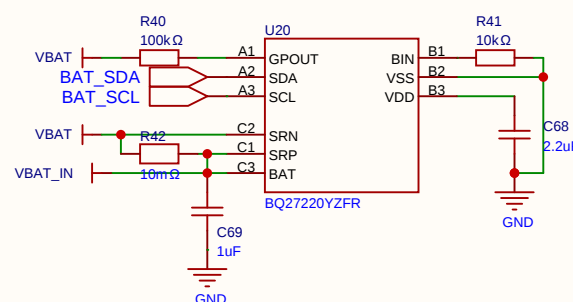
USB HOST POWER



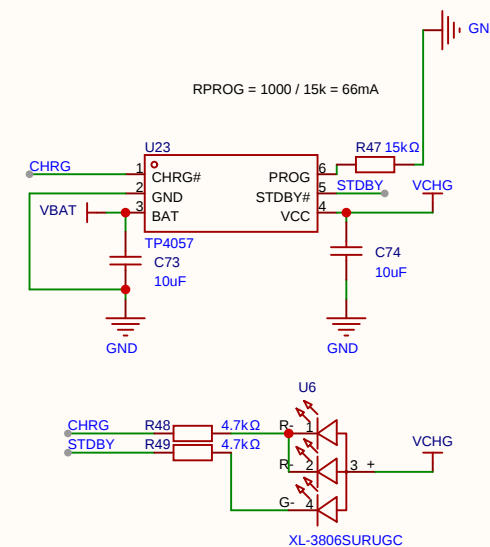
BAT CONNECT



BAT MANGAGE



BAT CHARGE



Schematic	SCH_ESP-Mosaico_CoreBoard_V1_0			Create Date	2026-07-23		
Board	ESP-Mosaico_CoreBoard_V1_0			Update Date	2026-08-18		
Version		ESP-Mosaico					
Size							
				Page	5	Count	5
 ESPRESSIF				Espressif			

